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Deborah M. Meredith
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Applicant : Esin Terzioglu, et al.
Application No. : 09/775,477
Filed : February 2, 2001
Title : MEMORY MODULE WITH HIERARCHICAL FUNCTIONALITY
Grp./Div. : 2811
Examiner : Not Yet Assigned

Docket No. : 40050/JFO/B600

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INFORMATION DISCLOSURE STATEMENT - 37 CFR § 1.97(b)

Assistant Commissioner for Patents
Washington, D.C. 20231

Post Office Box 7068
Pasadena, CA 91109-7068
June 5, 2001

Commissioner:

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, and in accordance with the provisions in the Manual of Patent Examining Procedure §§ 609 and 707.05(b), enclosed is FORM PTO-1449 listing the references that are known to applicant. Copies of each of the listed references are enclosed. This filing is timely because it is made during one of the periods described in 37 CFR § 1.97(b).

It is respectfully requested that the listed references be considered in the examination of this application and identified on the list of references cited on the patent issuing for this application. Applicant also requests that an initialed copy of FORM PTO-1449 be entered in the application file and returned to applicant with the next communication from the Office in accordance with MPEP § 609.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

By

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JFO/dmm

Enclosures: PTO-1449, w/references
DMM IRV1045486.1*-6/5/01 4:52 PM

FORM PTO-1449

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)



Attorney Docket Number 40050/JFO/B600

Application Number 09/775,477

Filing Date February 2, 2001

Applicant(s) Esin Terzioglu et al.

Group Art Unit 2811

Examiner Name Not Yet Assigned

#5/IDS

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5170375	12/08/92	Mattausch et al.	365	230.03	
	5752264	05/12/98	Blake et al.	711	144	
	5781498	07/14/98	Suh	365	230.06	
	5864497	01/26/99	Suh	365	72	
	6040999	03/21/00	Hotta et al.	365	200	
	6144604	11/07/00	Haller et al.	365	221	
	6141286	10/31/00	Vo et al.	365	230.03	
	6141287	10/31/00	Mattausch	365	230.03	
	6154413	11/28/00	Longwell et al.	365	230.03	
	6163495	12/19/00	Ford et al.	365	230.03	
	6166942	12/26/00	Vo et al.	365	63	
	6166986	12/26/00	Kim	365	230.06	
	6166989	12/26/00	Hamamoto et al.	365	233	
	6169701	01/02/01	Eto et al.	365	230.06	
	6173379	01/09/01	Poplingher et al.	711	165	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER INITIALS	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	KIYOO ITOH, et al., TRENDS IN LOW-POWER RAM Circuit Technologies, Proceedings of the IEEE, Vol. 83, No. 4, pages 524-543, April 1995.

EXAMINER SIGNATURE	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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